1) Locate IC1, LC7113 PLL chip. Very carefully, using an xacto knife with #11 C blade, cut away a small portion of the IC to expose Pin 9, (which has been clipped by manufacturer) and solder a wire onto it, using a grounded tip iron and very quickly, using as little heat as possible.

2) Connect another wire to Pin 1 of IC1, to pick up reg. 6 Volt.

3) Using a SPDT switch, connect as follows:

![Diagram showing wire connections to IC1]

4) Locate Pl/J301 BLUE wire. Cut BLUE wire in two and wire another SPDT switch as follows:

![Diagram showing wire connections to Pl/J301]

Adjust VCO, T1 as necessary.
This will give lots of low channels and high channels up to 27.595. For more up channels, change the value of X2. Changing X2 to an 18.2875 Xtal will give you up to 27.995 MHz.

Tune up:
Peak T3, T4, T5, L7, L10, L11. TC2 is TV1. VR6 is the driver bias. VR7 is the Final Bias. VR14 is the AM power. VR13 is the AM Modulation. VR11 is the Carrier Balance. VR4 is the SSB Modulation Gain. VR5 is the SSB Modulation Gain. VR8 is the RF Power meter.
Slider Info:

1) Cut BLUE wire off of PC board at R15 and ground.

2) Remove VR3 from board. Readjust VR1 and VR2 as necessary in relation with USB transmit frequency.