1. Wire up the SPDT switch and trim capacitor as shown in Fig.1.
2. Cut the foil trace between the 10.240MHz. crystal and C80 as shown in Fig.2.
3. Solder the wires from the switch across the cut trace.
4. With switch in low position, adjust VC for 27.410 on Ch. 40.
5. Switch to high position and check for 27.405. If necessary, alter the value of C80 to obtain this reading.

CHANNEL CONVERSION

1. Locate, unsolder, and lift the anode leg of D21 (off of pin 20 of the PLL chip LC7131).
2. Solder one leg of the 4700ohm resistor supplied to pin 20 of the PLL chip.
3. Run a wire from the other leg of the resistor to terminal Q of the DPDT switch provided.
4. Run a wire from terminal P on the switch to the unmarked post of the epoxy pak. Also run a wire from terminal P to the anode of D21.
5. Run a wire from terminal S on the switch to ground.
6. Unsolder and remove C84 and C85 (off pin 4 of TA7310 VCO/Mixer chip).
7. Solder one leg of the 47pf capacitor provided to pin 4 of the VCO/Mixer chip.
8. Run a wire from the other leg of the capacitor to terminal K on the switch.
9. Run a wire from terminal J on the switch to where the other leg of C84 was connected (R112).
10. Run a wire from terminal L on the switch to the yellow dot post of the epoxy pak.
11. Run a wire from the red dot post of the epoxy pak to pin 18 of the PLL chip.

Now this unit will operate on Channels 42-86, 1-40 and on half channels 1A-40A.